Chair of Security in Information Technology
TUM Department of Electrical and Computer Engineering
Technical University of Munich

TUM

Bachelor's Thesis

Machine Learning for Reverse Engineering - Design Flow

The final step of hardware reverse engineering is the identification of the functionality of a chip, after it has been delayered and imaged. This is often done by matching against known designs. Machine Learning methods can be applied to improve the identification. In this work, the applicability of pattern matching, classification alogrithms or Neural Nets should be tested and analysed.

Advisors

Johanna Baehr

Chair of Security in Information Technology
TUM Department of Electrical and Computer Engineering
Technical University of Munich