Chair of Security in Information Technology
TUM Department of Electrical and Computer Engineering
Technical University of Munich

TUM

Bachelor's Thesis

Automtation of Logic Locking Schemes

Logic locking has become one of the more promising methods to protect hardware designs against IP Infringement and Hardware Attacks - for example through the insertion of Hardware Trojans. This work should look into existing tools which automate the locking process, as well as automating known methods which are not yet included in existing tools.

Prerequisites

Idedaly:

- Python / C
- Verilog / VHDL

Contact

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