

Seminar

# Optimization methods applied to embedded systems design

Microelectronics-related problems include several complex sub-problems (e.g. placement, routing, partitioning, application mapping, power/thermal management, etc.) that are often classified as NP-complete or NP-hard. To solve these, several optimization methods (e.g. metaheuristics) coming from the operational research field can be used.

The goal of this seminar is to survey the sub-problems encountered in microelectronics design, show how they can be analyzed and modeled, and what kind of optimization methods can be used to solve them.

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