Chair for Integrated Systems
TUM Department of Electrical and Computer Engineering
Technical University of Munich

Seminar

Real-Time Application Mapping Methods for NoC-based Many-Core Systems

Modern Multi Processor Systems on Chip (MPSoC) can contain tens or even hundreds of cores. These systems usually run different applications in parallel, with some of them being real-time applications. When mapping these applications to the different cores of an MPSoC it is important to consider the communication requirements of each application and the dependencies between different applications to not use more resources than necessary and, by that, to minimize interference and power consumption. The goal of this seminar is to provide an overview of common methods for real-time application mapping in MPSoCs that use a Network on Chip (NoC) for communication.

Advisors

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