

Assistant (Student)

Power Modeling on FPGA for Approximate Computing

Approximate computing provides a new design paradigm by performing inexact calculations instead of the actual one. As a result, fewer resources are used in the FPGA devices, more functions can be implemented, and the energy efficiency of the calculations is improved.

This work is a part of approximate computing for image processing (ACIP) project and aims to develop a modular-based power model on FPGA in order to account for the power benefits from the different approximations.

Advisors

Manu Manuel