

Seminar

Inference on FPGA: A Survey of Technologies and Trends Towards Efficient Neural Network Accelerators

In recent years, the growing interest in deep learning across multiple disciplines has created a new competitive field in the semiconductor industry. The race towards providing the best hardware platform for training or inference has pushed some of the largest manufacturers into making acquisitions, changing some of the fundamental subcomponents of their existing solutions, and providing new solutions altogether.

The goal of this survey is to study the efforts made by the major FPGA manufacturers to push their platform as the ideal solution for deep neural network inference on edge devices.

Advisors

Nael Yousef Abdullah Al-Fasfous