

Master's Thesis

Implementation of a navigation clock assessment algorithm for space applications

- o Understand and replay the Simulations of the algorithm on Matlab Simulink
- o If necessary, adapt them for VHDL oriented implementation
- o Evaluate the FPGA technology, suitable for space, for the algorithm implementation
- o Evaluate the algorithm resources needs for FPGA implementation
- o Perform all steps to define, implement and test (vs Matlab) the algorithm into a the FPGA
- o Documentation associated to the VHDL development process

Advisors

Walter Stechele