Chair for Integrated Systems
TUM Department of Electrical and Computer Engineering
Technical University of Munich

Master's Thesis

Implementation of benchmarks for a new computing platform for space applications

o Evaluate the HPDP processor architecture for the required benchmarks
¤ Signal processing such as FIR, FFT
¤ I/O performance
o Partition the benchmarks in its sequential and parallel parts
o Implement the benchmarks in C and NML (native mapping language - a software dataflow description language) to achieve
best performance
o Debug and execute the benchmarks on the HPDP hardware
o Documentation of all the evaluation, implementation, various analysis and test results

Advisors

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