

Seminar

Word Identification on Gate-level Netlists

Word identification is an important step during reverse engineering of gate-level netlists. Identified words help to understand the general structure of a circuit and can therefore result in a better comprehension of the overall design functionality. There exist different approaches to identify and trace words within a netlist.

This seminar work should first give an overview of already existing word identification strategies. In a second step, it should compare and evaluate the different approaches.

References:

- Li, Wenchao, et al. "Wordrev: Finding word-level structures in a sea of bit-level gates." 2013 IEEE international symposium on hardware-oriented security and trust (HOST). IEEE, 2013.
- Tashjian, Edward, and Azadeh Davoodi. "On using control signals for word-level identification in a gate-level netlist." 2015 52nd ACM/EDAC/IEEE Design Automation Conference (DAC). IEEE, 2015.
- Meade, Travis, et al. "The old frontier of reverse engineering: Netlist partitioning." Journal of Hardware and Systems Security 2.3 (2018): 201-213.

Contact

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