

Bachelor's Thesis, Ingenieurspraxis, Interdisciplinary Project

Toggle Analysis Framework

Toggle Analysis is commonly used to evaluate if a hardware design does provide exploitable leakage i.e. leakage is modeled using information based on the toggle count.

In this work, a existing toggle analysis framework should be enhanced using a compiled language.

https://link.springer.com/chapter/10.1007/978-3-642-42033-7_25

Prerequisites

- Python
- C/Rust/WhateverAsLongAsItsFast
- Verilog / VHDL

Advisors

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