

Assistant (Student), Ingenieurspraxis

Designing a PCB for a RISC-V ASIC

Do you have a background in PCB design for digital microcontrollers?

Do you want to work on cutting-edge security research?

Do you like to work with a state-of-the-art RISC-V core?

We need you for a PCB design!

Prerequisites

- Firm knowledge in PCB design for digital circuits, including oscillator, SPI memory, voltage regulator design

Contact

If you are interested in this project, don't hesitate to contact me via

alex.hepp@tum.de

Advisors

Alexander Hepp