

Bachelor's Thesis

Analysis of the Effect of Netlist Errors on Machine Learning based Hardware Reverse Engineering

The final step of hardware reverse engineering is the identification of the functionality of a chip, after it has been delayered and imaged. This is often done by matching against known designs. Machine Learning methods can be applied to improve the identification. In this work, the applicability of pattern matching, classification algorithms or Neural Nets should be tested and analysed.

Prerequisites

python / C

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