

Bachelor's Thesis

# Implementation of an Error Correcting Code with Limited Magnitude for Finite Blocklength

Error-correcting (block) codes are important in many practical applications. For these applications, asymmetric channels are common cases both in fiber and free space. Furthermore, in memories like LSI/VLSI, ROM, and RAM, asymmetric errors happen. These are the faults that affect address decoders, word lines, power supplies, and stuck-faults in a serial bus.

Deppe and Lebedev developed new codes for these channels. The goal of the thesis is to analyse and simulate those codes.

## Advisors

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