Intel's IPU: Starting from the beginning

Intel develops Network Devices consisting of an FPGA and a general purpose processor. These are the so called IPUs. The goal of this Thesis/Position is to get such an IPU (Intel IPU F2000X) up and running and evaluates its potential. Here, the goal is to program a custom IPU application and evaluate metrics like latency, throughput, and many more under varying circumstances.

Prerequisites

- Basic Knowledge Linux Terminal
- Basic Knowledge C/C++
- Basic Knowledge of and about FPGAs

Advisors

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