Posting Title

Hardware Validation Intern (m/f/d)

Job Summary
Imagine what you could do here. At Apple, new ideas have a way of becoming extraordinary products, services, and customer experiences very quickly. Bring passion and dedication to your job and there's no telling what you could accomplish. Multifaceted, amazing people and inspiring, innovative technologies are the norm here. The people who work here have reinvented entire industries with all Apple Hardware products. The same passion for innovation that goes into our products also applies to our practices strengthening our commitment to leave the world better than we found it. Join us to help deliver the next groundbreaking Apple product!

Key Qualifications
- General knowledge of SoC and microcontroller/DSP architecture/design and industry standard interfaces
- Experience in python programming as well as C bare metal programming
- Strong problem solving and communication skills (English)
- Ability to work on multi-functional projects with a globally located team
- Knowledge of logic design, HDL/Verilog as well as logic simulation and HW/SW interaction is a plus
- Eagerness to learn new concepts and take up new challenges

Description
As an intern, you will work on real world challenges the team faces on upcoming products. To be successful in this position, you have a strong technical foundation, are able to show initiative, learn new technologies quickly, and have excellent interpersonal and communication skills. You are motivated and prepared to work independently. With mentorship, you will partner with your colleagues to ensure complex systems (ASICs) can be successfully verified. The focus of the position can be directed to either FPGA prototyping or validation SW or automation infrastructure depending on project needs as well as the skill-mix and preferences of the applicant.

Education
Currently enrolled in your penultimate year of studies in a CE, EE, CS or related field (or your final year for an internship which is required to complete your course).