

Seminar

Roofline Models for Manycore Architectures

The Roofline Model, presented by Williams et.al. in 2009, is an intuitive and visual way to illustrate the system's performance and to observe potential bottlenecks. Based on the proposal in 2009, various approaches have been presented recently to apply the Roofline Model to a wide range of Computer architectures.

This seminar puts the Roofline Model in the context of Manycore Architectures. It differentiates thereby between Homogeneous and Heterogeneous Architectures, as well as between traditional Multicores and Tile-based MPSoCs.

The task during the seminar consists of an exhaustive literature study on that topic in order to describe, analyze and compare the most important approaches. Individual adjustments of the the seminar topic according to the student's preferences is possible.

Prerequisites

B.Sc. in Electrical Engineering, Computer Science or a similar topic

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