

Master's Thesis

# Evaluate sensor processing algorithm on new computer platform

- o Get familiarised with the new computer architecture based on ARM Cortex R52 and with the typical sensor processing algorithm
- o Evaluate the computer hardware and software architecture for a sensor processing implementation
- o Perform an implementation of the algorithm with a selected software architecture
- o Evaluate the performance of the processing speed and computer resources (memory, interconnect bandwidth, ...)
- o Documentation of the implementation, evaluation and test results

## Advisors

Walter Stechele