

Seminar

Methods for on-chip hardware fault attack detection

Hardware fault attacks are a critical threat to the security of embedded systems. Detecting such attacks and introducing countermeasures on-chip can increase the security and privacy of an embedded system. While traditional countermeasures, analogue to safety, heavily rely on redundancy in the hardware/software design, a different approach could be to add more specific extensions to the hardware/software, thus, avoiding a multiplicity in overhead.

Contact

Johannes Geier (johannes.geier@tum.de)

Advisors

Johannes Geier