Internship/Working Student Opening in Infineon.

We are looking for internship/working student candidates at DES department in Infineon. If you are interested in the below topic and your background matches with the requirement, please contact us at this email: Saumya.Joshi@infineon.com

Description:-
Reliability is a major criterion for integrated circuits (ICs) in safety critical applications, such as automotive. A particular effect that contributes to wear out is transistor degradation via aging. It’s impact on the circuit behavior can be verified by circuit level aging simulations. Within this internship opportunity, main tasks would be in performing spice based aging simulations using our in house simulator for standard cells. The study will include comparison of aging impact in different technologies, and understanding where the variations come from.

Required Background:-
1. Experience with Spice based simulation.
2. Courses taken on: basic electronics/semiconductor devices, digital electronics.
3. Familiarity with schematic design is a plus.

Time frame: up to 6 months