

Seminar

# Security Awareness in High-Level Synthesis

High-level synthesis (HLS) uses advanced automated toolchains with optimization algorithms to achieve shorter development cost for complex designs. These optimization algorithms are for the HLS tools' backend stages, e.g., allocation, scheduling, and binding, and they are highly optimized for resources/latency constraints. Current HLS tools are unaware of a designs' security assets, such that their algorithms not always suitable for handling constraints to the design imposed by those security assets.

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