Electronic Design Automation
TUM Department of Electrical and Computer Engineering
Technical University of Munich

Seminar

Fast Cycle-Accurate Simulators for RISC Processors

Modern design approaches for embedded systems rely heavily on abstract models of the targeted hardware, to allow early and fast simulations of the system. Typical examples of such models are Virtual Prototypes (VPs) and Instruction Set Simulators (ISSs).

While VPs and ISSs offer high simulation speeds, they are not capable of providing reliable information regarding the systems performance (i.e. its timing behavior). Cycle Accurate Simulators (CASs) are capable of providing more accurate data on the systems performance, but at the cost of reduced simulation speeds.

During this project existing CASs (e.g. ComCAS, R2VM) shall be investigated.

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